



DTE Code: EN4139

Samridhi Sarwajanik Charitable Trust's  
**JHULELAL INSTITUTE OF TECHNOLOGY**

**An Autonomous Institute** affiliated to RTM Nagpur University

Off Koradi Road, Lonara, Nagpur - 441111.

Contact No.: 82086 39771, 82086 39501

E-Mail ID : admin@jitnagpur.edu.in Website : www.jitnagpur.edu.in

**Vision: To become an eminent institution through knowledge and research.**



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# ACADEMIC REGULATIONS

## Version 1.1

### (2024-25)

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## 1. Preamble

Jhulelal Institute of Technology (JIT) was established by the Samridhi Sarwajanik Charitable Trust in 2008 under the noble cause of promoting higher and technical education amongst deserving students of the region. The Chairman of the trust Shri. Mahesh Sadhwani, (B.E. is a Vice President of CREDAI Nagpur Metro and Ex President of Vidharbha Contractors Association Nagpur is also a well-known entrepreneur by profession. He is supported by a dedicated team of well-qualified technocrats and entrepreneurs as the Board of Directors and Governing Council Members.

JIT is headed by Principal Dr. Narendra Bawane (Ph.D.-VNIT, M. Tech-IIT Delhi) who has vast academic experience of more than 28 years to his credit including 12 years in Govt. organization. He completed his B.E. from Nagpur University in 1987 and M. Tech. in 1992 from IIT, New Delhi. He has completed his Ph. D. in 2006 from VNIT, Nagpur. He has worked as a Principal, Head of department and as a dean Autonomy at prestigious institutions during the last few years. He is a Ph.D. supervisor for Electronics and Electrical engineering in RTM Nagpur University and total 10 students completed Ph. D. under him. He is an IEEE senior member and an Execom member of IEEE, Nagpur subsection. He is currently member of board of studies, RTMNU, research committee of Nagpur & Amravati University. He is also on the academic advisory board of renowned institutes. He is also a life member of ISTE. He has published more than 102 research papers in International / National journals and conferences. He serves on the editorial board of reputed journals and technical committee member of reputed conferences. He serves on the editorial board of reputed journals and technical committee member of reputed conferences. He has received the Best Educationist Award from International Institute of Management, Delhi in 2012. He has received a grant of Rs. 23 lacs from AICTE for research in BCI and industry institute partnership cell. He is a resource person for the soft computing domain. His area of interest includes Hybrid intelligence, image processing, Brain computer interface, embedded system, Speech

processing etc. He handles several research projects and strongly believes in innovation and creativity.

JIT runs 7 UG, 3 PG and 3 Ph.D programs and Research center in Electronics Engineering, MBA & CSE approved under RTM Nagpur University. JIT is known for its state of the art infrastructure, internship opportunities, student activities, skill enrichment programs and social activities with emphasis on ethical, moral and patriotic values among the students.

The effective teaching-Learning process at JIT is executed by a team of well qualified, industry-experienced, and dedicated Faculty who practice an integration of practical, ICT, Virtual and other latest teaching technologies, to reach out to the learners. The quality of education imparted by JIT is reflected in the meritorious performance of its students at the University examinations, excellent Institute- Industry collaboration and notable placements. Apart from academic excellence, JIT has been supporting many high-end technical events like SHIKHAR, International conferences and Skill-India. This helps to nurture technical skills in the students. It also strives to inculcate a sense of social responsibility among students by providing them ample opportunities to participate in various National programs such as Woman Empowerment, Beti Bachao, Unnat Bharat Abhiyan etc.

**Vision:**

- To become an eminent institution through knowledge and research.

**Mission:**

- To produce world class engineers with academic and moral excellence who are not only equipped with cutting edge technological skills but also possess an immense sense of social responsibility.
- To inculcate awareness and acceptance of ethical values through co-curricular activities for the overall development of students.

**Objectives:**

- To be a premier institute for quality education and research in the field of Engineering and Management.
- To produce technically proficient, quality cognizant engineers imbued with moral and professional ethics.
- To strengthen interaction with industries and other organizations.
- To develop various laboratories of departments into centers of excellence.



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## 2. Abbreviations and Definitions

### Abbreviations

1	AICTE	All India Council for Technical Education.
2	AC	Academic Council
3	BE	Bachelor of Engineering as undergraduate degree awarded from University
4	B. Tech	Bachelor of Technology as undergraduate degree awarded from University
5	BoG	Board of Governors of the Institute.
6	BoS	Board of Studies
7	CGPA	Cumulative Grade Point Average
8	COE	Controller of Examinations
9	CAE	Class Assessment Examination
10	CBCS	Choice Based Credit System
11	CTS	Credit Transfer Scheme
12	DSY	Direct Second Year
13	DTE	Directorate of Technical Education, Government of Maharashtra
14	ESE	End Semester Examination
15	ES	Examination Scheme
16	GATE	Graduate Aptitude Test in Engineering
17	HOD	Head Of Department
18	JIT	Jhulelal Institute of Technology
19	JEE	Joint Entrance Examination
20	M.Tech.	Master of Technology Program
21	NMEICT	National Mission on Education through ICT
22	NEP	National Education Policy
23	PG	Post Graduate

24	RTMNU or University	Rashtrasant Tukadoji Maharaj Nagpur University
25	QEEE	Quality Enhancement in Engineering Education
26	SGPA	Semester Grade Point Average / Term Grade Point Average
27	State Government	Government of Maharashtra
28	TAE	Teacher Assessment and Evaluation
29	TC	Transfer Certificate
32	TS	Teaching Scheme
33	UG	Undergraduate degree awarded by University
34	UGC	University Grants Commission

### Definitions

1	Academic Autonomy	Freedom in all aspects of conducting academic programs, granted by University for academic excellence.
2	Academic Year	Two consecutive (one odd + one even) Terms constitute one academic year.
3	Anti-Requisite Course	Two or more courses can not be registered during degree.
4	Arrear Course	A course, not detained but not registered for exam i.e. course work/term completed but not registered for exam.
5	Autonomous Institution / College	An institution/college designated as autonomous by UGC under UGC Act 1956 & affiliated to Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur.
6	Backlog Course	Appeared for a course exam and not cleared (Pass) a course.
7	Branch	Specialization or discipline of B. Tech. Degree Programme, like Electrical Engineering, Computer Science Engineering, etc.
8	CGPA	It is a measure of overall cumulative performance of a student till that Terms.
9	Co-Requisite Course	Two or more courses have to be registered simultaneously (same term).

10	Course	Usually referred to as ‘papers’ is a component of a programme. All courses need not carry the same weightage. The courses should define learning objectives and learning outcomes. A course may be designed to comprise lectures/tutorials/laboratory work/field work/ outreach activities/project work/ vocational training/viva/ seminars/ term papers/assignments/ presentations/ self-study etc. or a combination of some of these.
11	Course Registration	Choice for selection of a course in the academic term.
12	Credit	A unit by which the course work is measured. One credit is equivalent to one hour of teaching (lecture or tutorial) or two hours of practical work/field work per week per term.
13	Detained course	The student registers for the course in a Term but does not complete 100 % term work or has poor attendance in the register course and is not allowed for an exam.
14	Dropped course	The student registers for the course in a Term & he/she will drop a course from a specific term within 2 weeks of start of the term, if a student feels that the registered course is difficult.
15	Dropping Term	Not registering a single course for the term, though eligible
16	Exam Registration	Giving willingness to appear for the exam
17	Grade	It is an index of the performance of students in a credit course. Grades are denoted by AA, AB, BB, BC, CC, CD, DD and FF and for audit course grades are denoted by A, B, C and D.
18	Grade Point	It is a numerical weightage allotted to each grade on a 10-point scale for credit courses.
19	Honors/Minor	Degree with Major / Minor Specialization
20	Core Subject	All compulsory courses for all Programs.
21	Elective Subject	Compulsory courses from the list of choices common to all Programs.

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### 3. About Departments

#### 3.1 Department of Applied Science & Humanities

The Department of Applied Sciences and Humanities teaches and promotes practical aspects of Applied Sciences and Humanities to build a solid foundation as part of the Engineering Education. Department imparts the knowledge of fundamental sciences and mathematics to students of all the engineering disciplines as the backbone of engineering and technology is formed by these subjects. So, there is a need to build a strong foundation for the prospective and promising Engineers.

The First Year students are initiated into a unique way of learning and are groomed to take up their roles in society not only as good engineers but as good human beings. A team of dedicated staff members train the students for higher engineering subjects focusing on the academic as well as the overall development through activities like skill development programs, mentoring, one day trips, cultural and sports activities.

Engineering Mathematics, Physics, Chemistry, Communications Skills, Introduction to Programming concepts etc. are the few subjects that are taught through lectures, practicals and tutorials. English Communication skills laboratory is an integral part of the department. Students receive training in written and verbal communication essential to succeed in the workplace.

Some Basic Engineering subjects like “Basics of Electrical and Electronics engineering” builds up the elementary understanding and inclined the students towards developing an engineering mindset. The Department is backed by well-educated, talented and dedicated faculty members to fulfill this task. The teaching staff of the department have published many papers in National and International journals. The subjects taught by teachers of this department are of prime importance for all round growth and development of students.

### **3.2 Department of Computer Science and Engineering**

The department provides students with better computational infrastructure, knowledge in recent & advanced technologies so as to contribute to emerging as one of the best technology departments. The department strives to help students build learning & adaptability in developing technical know-how through collaborative research more. The department is dedicated to fostering innovation, excellence, and creativity in the fields of computer science and engineering. With a focus on cutting-edge technologies, practical skills, and theoretical foundations, the department strives to equip our students with the knowledge and expertise needed to tackle the challenges of the digital era.

The Computer Science and Engineering department provides a dynamic learning environment, state-of-the-art infrastructure, and a team of passionate faculty members who are experts in their respective fields. The Department believes in a hands-on approach to education, combining theoretical concepts with real-world applications, and encouraging students to think critically, solve complex problems, and push the boundaries of technology.

The Computer Science and Engineering department also offers M. Tech. (Computer Science & Engineering) Program & Ph.D Program by Rashtrasant Tukdoji Maharaj Nagpur University, Nagpur..

### **3.3 Department of Computer Science and Engineering (Artificial Intelligence & Machine Learning)**

The department provides students with better computational infrastructure and knowledge of recent & advanced technologies so as to contribute to emerging as one of the best technology departments. The department strives to help students build learning & adaptability in developing technical know-how through collaborative research. In a world driven by data and automation, the Department of Computer Science and Engineering (Artificial Intelligence & Machine Learning) offers an exciting and transformative academic journey. As a pioneer in the field of Artificial Intelligence and Machine Learning, the department is dedicated to unleashing the full potential of these revolutionary disciplines. The department is a thriving hub of innovation, where students embark on a transformative learning experience that equips them with the knowledge, skills, and expertise to navigate the rapidly evolving landscape of AI and machine learning. With a distinguished faculty at the forefront of research and a state-of-the-art infrastructure, the department provides an environment that cultivates creativity, critical thinking, and hands-on exploration.

### **3.4 Department of Electrical Engineering**

The Department of Electrical Engineering was established in the year 2014-2015. Currently, the department offers Undergraduate degree course in Electrical Engineering. The department has an elite group of faculty members who are dedicated and well experienced in different fields of specialization. The department is fully equipped with the latest systems and apparatus in the Electrical machines, Power Electronics, Network Theory, Internet of Things Applications in Electrical Engineering, Control Systems, Power System Simulation, Embedded Systems, Measurements and Instrumentation laboratories and also deals with Artificial Intelligence applications. The Department has an specialization of Electric Vehicles and Renewable Energy in Projects. The Department faculties encompass all the sub-disciplines of Electrical Engineering and are active in research in all the areas. They also collaborate with the faculty from other disciplines within and outside the institute. Faculty members have published papers in refereed International/National journals and conferences. The Department linked with The Department faculties embrace in Patents, copyrights and other IPR activities. The Department's faculty members have authored several books. Also department faculties involves as a Resource Person at other in FDPs at reputed institutions and Reviewer at IEEE Conferences/Journals. The research activity involves active participation from the students, faculty and the industry. The Department has received research project funds from different funding agencies.

### **3.5 Department of Electronics and Telecommunication Engineering**

Electronics and Telecommunication Engineering is a constantly changing and widening branch of engineering. Increased production and the demand by the government, business community, and corporate world contribute to the growth of employment opportunities in the field. Department of Electronics and Telecommunication Engineering of Jhulelal Institute of Technology, Nagpur is one of the premier departments aimed to provide quality education to the young engineers in the field. The department has excellent infrastructure and the resources in the form of modern equipment, well qualified faculty members, state-of- the-art library which plays an important role in shaping the career of the undergraduate and postgraduate scholars in and around the country. The department plays a pivotal role in laying down road maps in the different research areas such as Power Electronics, RF and Microwave Communications, Signal and Image processing, Microelectronics and VLSI/Embedded Systems. The experienced and learned team of 9 Faculty members having average experience of 10 years, 1 technical supporting staff and 2 non teaching staff are

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guiding the students due to which the results of the department stand good in the examinations.

### **3.6 Department of Electronics & Communication (Advanced Communication Technology)**

The global communications landscape has undergone a sea change, with new technology evolving every day, and out-of-the-box thinking. Terrestrial and satellite communications have long existed for a good number of decades in the past, as independent domains, but the future goes beyond these boundaries – a future of hybridized connectivity, and here's where the graduate course on Electronics & Communication (Advanced Communication Technology), offered by the Jhulelal Institute of Technology an autonomous institute, comes in. Through this course, students are groomed and prepared to make intellectual contributions, embark on a lifelong career of personal and professional growth, and take up specializations at the Postgraduate level. The Electronics & Communication (Advanced Communication Technology) department presents brilliant academic activities in the domains of wireless and optical communication, signal processing and computer vision, wireless sensor networks, and big data mining, and cognitive radio, in this program. The students doing this course on Electronics & Communication (Advanced Communication Technology) are trained with the help of a state-of-art Curriculum, which provides the strong foundation that they need for meeting their future responsibilities as leaders and researchers of upcoming communications applications.

### **3.7 Department of Electronics and Computer Engineering**

Due to technological advancements in electronic & computer systems, the demand for skilled electronics engineers with computer knowledge has increased significantly. Electronics and Computer Engineering - a future of hybridized connectivity provides students skills and knowledge to design, develop and test electronic systems software applications. Electronics and computer engineers design and develop intelligent systems using artificial intelligence and machine learning for improving decision making in automation.

The Electronics and Computer Engineering department presents brilliant academic activities in the domains of designing, developing and maintaining complex electronic systems in industries, aerospace, automation and healthcare telecommunications. The department has excellent infrastructure and resources in the form of modern equipments, well qualified

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faculty members and a modern library which plays an important role in shaping the career of the undergraduate and postgraduate scholars. The students doing this course in Electronics and Computer engineering are trained with the help of a state-of-art Curriculum, which provides the strong foundation that they need for meeting their future responsibilities as leaders and researchers of upcoming times. Electronics and Computer Engineering is a critical branch of engineering field which prepares students to meet the demands of modern industry and contribute to the betterment of society.


### **3.8 Department of Mechanical Engineering**

Throughout human history, engineering has been a driving force behind the advancement of civilization, and mechanical engineering has played a pivotal role in this evolution. The transition from the Stone Age was marked by the metallurgists who pioneered the use of copper and iron, setting the stage for future innovations. Shipbuilders connected disparate parts of the world through enhanced travel and trade, revolutionizing global interaction. The development of increasingly sophisticated agricultural tools and textile production technologies transformed daily life and industry. Mechanical clocks and the printing press introduced fundamental changes in how people measured time and disseminated information.

In more recent times, the Industrial Revolution represents a monumental leap, with mechanical engineering at its heart. This era saw machines take over many human tasks, amplifying productivity and reshaping labor dynamics. The steam engine, a hallmark of mechanical ingenuity, powered advancements in mining, transportation via trains and ships, and fundamentally altered various industries. Additionally, improved sanitation systems, a product of engineering innovations, significantly boosted public health in the Western world. Mechanical engineering continues to drive progress, reflecting its integral role in shaping modern civilization.

### **3.9 Department of Business Administration**

The Department of Business Administration was established in 2009. Currently, the Department offers a Master of Business Administration (MBA) full-time Post- Graduate Program specializing in Finance, Marketing, Operations, Human Resource Management, and Business Analytics with a sanctioned intake of 180 seats. We, at JIT strive to meet the growing requirements of updated management education with innovation and a constructive approach. Programme provides a good blend of theoretical knowledge coupled with practical result-oriented training. The Department has a dedicated Training and Placement Cell with a good placement ratio of students. The Department is recognised as a Ph.D. Research Centre by Rashtrasant Tukdoji Maharaj Nagpur University, Nagpur.



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Towards Excellence


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#### 4. Programmes Offered (UG/PG)

Course Level	Sr. No		Course	Intake (2023-24)	Intake (2024-25)
Undergraduate	1	CS	B.Tech in Computer Science & Engineering	150	180
	2	AI	B.Tech in Computer Science & Engineering (Artificial Intelligence & Machine Learning)	120	180
	3	EE	B.Tech in Electrical Engineering	30	60
	4	ER	B.Tech in Electronics & Computer Engineering	-	60
	5	ET	B.Tech in Electronics and Telecommunication Engineering	30	-
	6	EC	B.Tech in Electronics & Communication (Advanced Communication Technology)	60	60
	7	ME	B.Tech in Mechanical Engineering	30	30
Post Graduate	1	MB	Master of Business Administration	180	120
	2	MC	M.Tech in Computer Science & Engineering	24	24
	3	MV	M.Tech in VLSI	24	24

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## 5. Academic Marks Policy for UG (Theory, Practical, Project & Incentives)

<b>Evaluation of Theory Course</b>		
Continuous Assessment	End Semester Examination	Total Marks
40	60	100
20	30	50

<b>Continuous Assessment</b>					
CAE-I	CAE-II	TAE-I	TAE-II	Attendance	Total Marks
15	15	3	3	4	40
6	6	2	2	4	20

<b>Attendance Marks (4 Marks)</b>				
< 75%	75% to 81%	82% to 88%	89% to 94%	95% to 100%
0	1	2	3	4

### Teacher Assessment and Evaluation

(Minimum 2 set of activities, each activity max 2 or 3 marks, Activities should not be repeated for the same set of students)

Assignments, Case Study, Poster Presentation, Individual Subject topic Presentation, Open Book Test, Puzzles, Any subject specific activity, Video Assessments, Group Activity (Guest Lecture, Workshop, Debate, Discussion, NPTEL Video understanding, Role-Playing, Quizzes and Polls), Drill and Practice, etc...[Allot marks with proportionate]

<b>Evaluation of Practical</b>		
<b>Continuous Evaluation</b>	<b>End Semester Examination</b>	<b>Total Marks</b>
25	25	50

<b>Continuous Evaluation of Practical (25 Marks)</b>				
Write-up	Performance	Result & Interpretation	VIVA	<b>Total</b>
3	2	3	2	10*
* Average of minimum 10-14 practicals has to be considered for calculating internal practical marks out of 10.				
<b>Internal Practical Examination (15 Marks)</b>				
Write-up	Performance	Result & Interpretation	VIVA	<b>Total</b>
3	4	3	5	15

<b>End Semester Practical Examination (25 Marks)</b>				
Write-up	Performance	Result & Interpretation	VIVA	<b>Total</b>
5	5	5	10	25

<b>Mini Project / Third Semester Community Engineering Project (25 Marks)</b>				
Synopsis	Seminar-I	Seminar-II	Project Submission (Canvas, Poster & Report)	<b>Total</b>
5	5	5	10	25
<b>Note-</b> Students or project groups who win the project competition will be awarded 10 marks as an incentive in their internal assessments.				

<b>Major Project</b>							
<b>Phase I</b>	Abstract / Synopsis	Introductory Seminar	Progress Seminar	Final Phase – I Project Seminar	Review Paper Publication	Participation in Project Competition	<b>Total</b>
		10	5	5	10	15	5
<b>Phase II</b>	Progress Seminar	Pre submission Seminar	Final submission Seminar	Participation in Project Competition	Project Completion (Submission of Hardware / software project, Poster, Thesis etc.)	Final Paper Publication and Patent Filing	<b>Total</b>
	5	5	5	10	10	15	50
<b>Note-</b> Students or project groups who win the project competition will be awarded 10 marks as an incentive in their internal assessments.							

<b>Over and Above Marks (Max 50 Marks per semester) (Max 2 different Activities per Category)</b>						
<b>Category</b>	<b>T1</b>		<b>T2</b>		<b>T3</b>	
Technical Event Participation	IIT/ IIIT/ NIT/ Govt. Universities		Autonomous/NBA/NAAC Accredited Institutes		Any other Institute than T1 and T2	
	Winner	Participation	Winner	Participation	Winner	Participation
	15	10	12	8	10	6
Research	Patent		Research Publication		Copyright	Conference
	Granted	Published	WoS, Scopus	Any Other (UGC)	Published	Presentation
	15	10	12	8	5	5
Internship & Placement	Placement		Internship with stipend		Internship without stipend	
	20		15		5	
Skill Courses / Competitive Exam	Competitive Exam (GATE/CAT/MPSC/ JAT, etc.)		Other Online Exam MOOC		NSDL Certification Level 5 & above	
	Qualified	Participation	Qualified	Participation	10	
	15	10	12	8		
Student Participation	Received Fund / Sponsorship for Activity		Secured Top 5 Position in Previous Results at Institute		NCC/NSS/SRC Office Bearer	
	15		10		5 per Activity	

- Over and above Maximum 5 marks in each subject (Applicable for Theory/Practical/Project) will be awarded only upto maximum internal marks.
- Process for permission & other formalities should be done through respective portfolios in-charge.
- Submission of relevant certificates/ proof in person to respective portfolio in-charge.
- Subjects, which have different marking schemes, may apply proportional marking schemes.



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## 6. Academic Marks Policy for PG (Theory, Practical, Project & Incentives)

Evaluation of Theory Course		
Continuous Assessment	End Semester Examination	Total Marks
40	60	100
20	30	50

Continuous Assessment					
CAE-I	CAE-II	TAE-I	TAE-II	Attendance	Total Marks
8	8	8	8	8	40
4	4	4	4	4	20

Attendance Marks (4 Marks)			
< 75%	75% to 79%	80% to 85%	86% to 100%
0	4	6	8
0	2	3	4

Evaluation of Practical		
Continuous Evaluation	End Semester Examination	Total Marks
25	25	50
50	50	100

Continuous Evaluation of Practical				
Write-up	Performance	Result & Interpretation	VIVA	Total
3	2	3	2	10*
6	4	6	4	20*

\* Average of minimum 10-14 practicals has to be considered for calculating internal practical marks out of 10 or 20.

Internal Practical Examination				
Write-up	Performance	Result & Interpretation	VIVA	Total
3	4	3	5	15
6	8	6	10	30



End Semester Practical Examination				
Write-up	Performance	Result & Interpretation	VIVA	Total
5	5	5	10	25
10	10	10	20	50

<b>M.Tech Final Year Major Project</b>							
<b>Phase I</b>	Synopsis Submission	Introductory Seminar	Progress Seminar	Final Phase – I Project Report	Survey Paper Publication (Scopus only)		<b>Total</b>
	10	5	5	10	20		
<b>Phase II</b>	Progress Seminar	Pre submission Seminar	Final submission Seminar	Participation in Project Competition	Project Completion (Submission of Hardware/software project, Poster, Thesis etc.)	Final Paper Publication (Scopus/UGC only) and Patent Filing	<b>Total</b>
	10	10	20	30	50	30	
<p><b>Note-</b> Students or project groups who win the project competition will be awarded 10 marks as an incentive in their internal assessments.</p>							

<b>M.B.A Final Year Major Project</b>					
<b>Phase I</b>	Introductory Seminar	Submission of Review of Literature	Progress Seminar	Synopsis Submission	<b>Total</b>
	10	10	10	20	
<b>Phase II</b>	Progress Seminar	Final submission Seminar	Final Paper Publication (Scopus/ UGC only)	Project Thesis/Report Submission	<b>Total</b>
	10	10	10	20	
<p><b>Note-</b> Students or project groups who win the project competition will be awarded 10 marks as an incentive in their internal assessments.</p>					

<b>Over and Above Marks (Max 50 Marks per semester) (Max 2 different Activities per Category)</b>						
<b>Category</b>	<b>T1</b>		<b>T2</b>		<b>T3</b>	
Technical Event Participation	IMT/IIM/Symbiosis		Autonomous/NBA/NAA C Accredited Institutes		Any Other Institute than T1 & T2	
(Case study presentation, Debate competition, Group Discussion, Project presentation, Start up idea discussion)	Winner	Participation	Winner	Participation	Winner	Participation
	15	10	12	8	10	6
Research	Patent		Research Publication		Copy-right	Conference
	Granted	Published	Scopus	UGC Journal	Published	Presentation
	15	10	12	8	5	5
Internship & Placement	Placement		Internship with Stipend		Internship without stipend	
	20		15		5	
Skill courses/Competitive Exam	(Competitive Exam NET/SET/PET/UPSC/MPSC)		Online Course		NISM/Equal weight age Certification	
	Qualified	Participation	Qualified	Participation	10	
	15	10	12	8		
Student Participation	Received Fund / Sponsorship for activity		Session in industry as a trainer		Peer Learning (Session Delivery)	
	15		10		5	

- Over and above Maximum 5 marks in each subject (Applicable for Theory/Practical/Project) will be awarded only upto maximum internal marks.
- Process for permission & other formalities should be done through respective portfolios in-charge.
- Submission of relevant certificates/ proof in person to respective portfolio in-charge.
- Subjects, which have different marking schemes, may apply proportional marking schemes.

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## 7. Regulations for Program & Course Code

### Program & Course Code for UG

Year	Range of Code	Semester Wise Code
Applied Science & Humanities	100 to 199	Semester I: 100 to 149
		Semester II: 150 to 199
Second Year	200 to 299	Semester III: 200 to 249
		Semester VI: 250 to 299
Third Year	300 to 399	Semester V: 300 to 349
		Semester VI: 350 to 399
Final Year	400 to 499	Semester VII: 400 to 449
		Semester VIII: 450 to 499

### Program & Course Code for PG

Year	Range of Code	Semester Wise Code
First Year	500 to 599	Semester I: 500 to 549
		Semester II: 550 to 599
Second Year	600 to 699	Semester III: 600 to 649
		Semester VI: 650 to 699

**Format : XX YY ZZZ W**

**AS BS 100 T**

<b>XX</b>	<b>YY</b>	<b>ZZZ</b>	<b>W</b>
<b>AS:</b> Applied Science & Humanities <b>ET:</b> Electronics & Telecommunication <b>CS:</b> Computer Science & Engineering <b>AI:</b> Computer Science & Engineering (Artificial Intelligence & Machine Learning) <b>EE:</b> Electrical Engineering <b>ME:</b> Mechanical Engineering <b>EC:</b> Advance Electronics Communication <b>ER:</b> Electronics & Computer Engineering <b>MB:</b> Master of Business Administration <b>MV:</b> M.Tech (VLSI) <b>MC:</b> M.Tech (Computer Science Engineering)	<b>BS:</b> Basic Science Course <b>PC:</b> Programme Core Course <b>PE:</b> Programme Elective Course <b>MD:</b> Multi Disciplinary Minor <b>OE:</b> Open Elective <b>VS:</b> Vocational & Skill Enhancement Course <b>AE:</b> Ability Enhancement Course <b>HM:</b> Humanities Social Science & Management <b>IK:</b> Indian Knowledge System <b>VEC:</b> Value Education Course <b>RM:</b> Research Methodology <b>CP:</b> Community Engg. Project <b>MP:</b> Major Project <b>CC:</b> Co-Curricular Courses <b>OT:</b> Internship / On Job Training <b>HO:</b> Course for Honour <b>HR:</b> Course for Honour with Research <b>DM:</b> Course for Double Minor <b>EC:</b> Exit Credit Course	as per given in table Program & Course Code for UG & PG	<b>T :</b> Theory <b>P :</b> Practical

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## 8. Regulations for Promotion Rule

1. For being eligible to register for Semester III, students must have secured at least 50% of the total credits (rounded off to nearest lower integer) in first year (Semester I & II together).
2. For being eligible to register for Semester V, students must have completed successfully all courses & earned all the credits offered in first year and secured at least 50% of the total credits (rounded off to nearest lower integer) in second year (Semester III & IV together).
3. For being eligible to register for Semester VII, students must have completed successfully all courses & earned all the credits offered in first & second year and secured at least 50% of the total credits (rounded off to nearest lower integer) in third year (Semester V & VI together).



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## **9. Regulations for Attendance, Absence, Leave Rules, Detention and Dismissals**

1. A student must have an overall 75 % attendance of the total number of classes including lectures/ tutorials and practicals / NCC / NSS / guest lecture/field project/activities/internship/industrial visits/Games & Sports / Yoga/etc scheduled for them.
2. Shortage of attendance up to 15% i.e., attendance between 60% to 75% in aggregate, may be condoned by the Institute Academic Committee. Shortage of attendance below 60 % shall in no case be condoned.

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## 10. Regulations for Multiple Entry and Exit Path and Lateral Entry

### 10.1 Multiple Exits

Students will have the flexibility to enter a programme in odd semesters and exit a programme after the successful completion of even semesters as per their future career needs. Exit options shall be provided with Certification, Diploma and B. Vocational degrees to the students at the end of the second, fourth and sixth semester, respectively, in the four-year degree programme. Students will receive a Bachelor's degree with the single minor on successfully completing all eight semesters of the UG Programmes either at a stretch or with opted exits and re-entries. In addition to this, students will receive a Bachelor's degree with Double Minor/Honors/ Research subject to earning additional 20 credits.

1. Students exiting the First Year programme after securing minimum 40 credits will be awarded UG Certificate in the relevant Discipline /Subject provided they secure 8 credits in work-based vocational courses or internship / Apprenticeship offered during summer vacation in addition to 4 credits from skill-based courses earned during the first and second semester.
2. Students exiting the Second Year Programme after securing minimum 80 credits will be awarded UG Diploma in the relevant Discipline /Subject provided they secure additional 8 credits in skill-based vocational courses (skill-based courses, internship, mini projects etc) offered during summer vacation after the second year.
3. Students exiting the 3-year UG program will be awarded B. Vocational in the relevant Discipline /Subject upon securing minimum 120 credits with additional 8 credits in skill-based vocational courses (skill-based courses, internship, mini projects etc.) offered during summer vacation after the sixth semester.

## 10.2 Re-entry or Lateral Entry

Students, opting for exits at any level, will have the option to re-enter the programme from where they had left off, in the same or in a different higher education institution within four years of exit and complete the degree programme within the stipulated maximum period of eight years from the date of admission to first year UG. Re-entry at various levels for lateral entrants in academic programmes shall be based on the earned and valid credits as deposited and accumulated in the Academic Bank of Credits (ABC) through Registered Higher & Technical Education Institutions (RHTEI) and proficiency test records.

Lateral entry into the programme of study leading to the UG Diploma/ B. Vocational/ UG Bachelor's Degree with single minor/ UG Bachelor's Degree with Double Minor/ Honors /Research will be based on the validation of prior learning outcomes achieved and subject to availability based on intake capacity.

Eligibility for admission to the UG Bachelor's Degree with Double Minor/ Honors /Research as per UGC guidelines: Minimum CGPA/CPI of 7.5 or minimum 75% after second semester for UG Bachelor's Degree with Double Minor/ Honors and Minimum CGPA/CPI of 7.5 or minimum 75% after sixth semester for UG Bachelor's Degree with Research.

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## 11. Regulations for Honors, Honors with Research, Double Minor

Apart from the minimum credit requirements of 160 for the award of the undergraduate engineering degree, these schemes provide opportunities for supplementing the learning experience by crediting additional courses, in parent as well as in diverse areas. These additional credits when they are in a focused branch would earn the students credentials like Honours, Honours with Research, Double Minor.

Honors scheme aims for vertical knowledge growth in his/her own branch, which may have research orientation while Minor scheme aims for additional knowledge in any other branch for enhancement of employability. On successful completion of the requirements of Honours, Honours with Research, Double Minor schemes, the UG students shall be awarded a Certificate by JIT.

Participation of students in these schemes shall not be mandatory. Aspiring students will register for additional theory courses and acquire additional 20 credits for any one of the three schemes.

A student opting for 'Honors' will not be entitled to register for 'Minor' and vice-versa. It is expected that the students with good academic standing, utilize their surplus time for enhancing their academic learning experience and gain a wide exposure.

### **Additional Credits for Bachelor's Degree- with Honours: 20 Credits**

These are additional credits to be offered from the second year to the final year and will be offered as an option to students.

Under Bachelor's Engg./ Tech. Degree in chosen Major Engg./ Tech. With a Multidisciplinary and Specialization Major, 180 credits, students would take up five-six

additional courses of 20 credits in the same Engg./ Tech. discipline distributed over semesters III to VIII.

The decision regarding the mechanism of distribution of these 20 credits over semesters III to VIII, which are over and above the 160 Credits prescribed for Four Year Multidisciplinary Bachelor's Degree in Engg./ Tech., will be taken by JIT.

**Additional Credits for Bachelor's Degree- Honours with Research: Minimum 20 Credits**

These are additional credits to be offered in the final year and will be offered as an option to students. The UGC Regulations, 2021 permit up to 40% of the total courses being offered in a particular programme in a semester through the Online Learning Courses offered through the SWAYAM platform and/or other State Level Common Platforms which can be developed in due course with the participation of different Universities.

Under Bachelor's Engg./ Tech. Honours with Research Degree in chosen Major Engg./ Tech. Disciplined with Multidisciplinary Minor (180 credits), the students will work on a research project from a research center or dissertation for 20 credits in the fourth year in the respective Major Engg./ Tech. Discipline.

The decision regarding the distribution of 20 credits for the Research Project in Semesters VII and VIII of the Fourth Year will be taken by Academic Authorities of JIT. These 20 Credits will be over and above the 160 Credits prescribed for Four Year Multidisciplinary Bachelor's Degree in Engg./ Tech. Program.

**Additional Credits for Bachelor's Degree- with Double Minor: 20 Credits**

These are additional credits to be offered from the second year to the final year and will be offered as an option to students.

Under Bachelor's Engg./ Tech. Degree in chosen Major Engg./ Tech. Disciplined with Double Minor (Multidisciplinary and Specialization Minor, 180 credits), students would take up five-six additional courses of 20 credits in another Engg./ Tech. discipline/ Emerging Areas Specialization distributed over semesters III to VIII.

The decision regarding the mechanism of distribution of these 20 credits over semesters III to VIII, which are over and above the min.160 Credits prescribed for Four Year Multidisciplinary Bachelor's Degree in Engg./ Tech., will be taken by JIT.

### **Duration of Program with Honors**

All requirements of the program and Honours, Honours with Research, Double Minor have to be completed within the stipulated period of the original program for UG students who were admitted in First Year or through lateral entry in second year of the program. No additional period will be permitted.

If a student is unable to earn additional 20 credits along with all the prescribed credits of the parent program within the stipulated allowed duration of the parent program, he/she will not be awarded Honours, Honours with Research, Double Minor.

The Honours, Honours with Research, Double Minor courses completed if any by such students shall not be adjusted or converted into program credits anywhere in the 160 credits structure of the original curriculum of the program in which they were admitted and such additional credits will remain extra.



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## 12. Regulations for Amendment of Rules & Regulations

JIT may, from time to time revise, amend or change the regulations, scheme of examinations and syllabus if found necessary. Notwithstanding all that has been stated above, the Chairman, Academic Council is vested with powers to modify any or all of the above regulations from time to time, if required, subject to the approval by the Governing Body.



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### **13. Regulations for Emergent Cases**

Notwithstanding anything contained in the above regulations, the Chairman of the Governing Body / Academic Council may, in emergent situations can or may take action on behalf of the Governing Body / Academic Council, as he deems appropriate and report it to the next meeting of the Governing Body / Academic Council for its approval.