



DTE Code: EN4139

Samridhi Sarwajanik Charitable Trust's
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Vision: To become an eminent institution through knowledge and research.



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EXAMINATION CELL**RESULT OF M.TECH II SEM SUMMER-2025 (RESIT) EXAM**

BRANCH: V.L.S.I.

DATE: 13-08-2025

| SR. NO | Unique ID | SUBJECTS → | 1. VLSI Design for Analog Circuits Course Code (MVPC550T) | 2. VLSI Design for Analog Circuits Lab Course Code (MVPC550P) | 3. Modeling of Digital Systems Design Course Code (MVPC551T) | 4. Modeling of Digital Systems Design Lab Course Code (MVPC551P) | 5. Audio & Speech Signal Processing Course Code (MVPC552T) | 6. PEC-II (Machine Learning & its Engineering Applications) Course Code | 7. MDM-II (Web Development / Python Scripting) Course Code | 8. Advanced Embedded System Equipment Maintenance Lab | SGPA | Result | CGPA |
|--------|------------|--|--|--|---|---|---|--|---|---|-------|---------------|-------|
| | | STUDENT NAME | Grade | Grade | Grade | Grade | Grade | Grade | Grade | Grade | 10.00 | PASS/ FAIL | 10.00 |
| 1 | PGF24MV007 | VISHAKHA PATIL (Vishakha Arvind Meshram) | | AB | | | | | | | 8.54 | PASS | 8.29 |

.....' Already cleared in previous attempt.

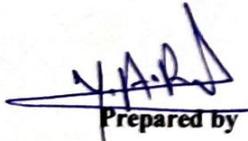
'FF' - FAIL

'U' - Unfair means in End Semester Examination

'SF' - Satisfactory, 'USF' - Unsatisfactory

'I' - means Incomplete course requirement due to absence in End Semester Examination

Note: This result is subjected to corrections.



Prepared by

ASSISTANT TO COE



JOINT CONTROLLER OF EXAMINATIONS



CONTROLLER OF EXAMINATIONS

Controller of Examinations
 Jhulelal Institute of Technology



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